



Attorney Docket No. 1081.1180

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Toshio ARAKAWA

Application No.: 10/634,857

ATTENTION

Confirmation No.: 6516

CERTIFICATE OF CORRECTION

Filed: August 6, 2003

BRANCH

U.S. Patent No.: 6,990,642

Issued: January 24, 2006

For: DESIGN METHOD FOR INTEGRATED CIRCUIT HAVING SCAN FUNCTION

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate

MAY 04 2006

of Correction

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 USC § 254 and 37 CFR § 1.322, to correct the error(s) shown on the attached Form PTO-1050. A check in the amount of \$100 to cover the cost of the Certificate is attached.

One or more of the errors shown on the attached Form PTO-1050 is or are within the responsibility of the undersigned; each thereof is of a clerical or typographical nature or of minor character and occurred in good faith, and the correction thereof is consistent with the prosecution record.

Respectfully submitted,

STAAS & HALSEY LLP

Date: April 21, 2006

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,990,642

DATED : January 24, 2006

INVENTOR(S) : Toshio ARAKAWA

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

First Page, Column 2 (Abstract), Line 6, delete "flip-flops" and insert - - flip flops - - therefor.

First Page, Column 2 (Abstract), Line 10, delete "laid-out" and insert - - laid out - - therefor.

First Page, Column 2 (Abstract), Line 13, delete "flip-flops" and insert - - flip flops - - therefor.

First Page, Column 2 (Abstract), Line 15, delete "flip-flops" and insert - - flip flops - - therefor.

First Page, Column 2 (Abstract), Line 17, delete "laid-out" and insert - - laid out - - therefor.

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PATENT NO. 6,990,642

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